0057-2362-2YY

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RECHIEDE

IN THE UNITED STATES PATENT & TRADEMARK OFFICE OF TECHNOLOGY

IN RE APPLICATION OF:

SHIGENOBU MAEDA ET AL.

: GROUP ART UNIT: 2811

SERIAL NO: 09/176,315

FILED: OCTOBER 22, 1998

: EXAMINER: CRANE, S.

FOR: METHOD OF DESIGNING SEMI-

CONDUCTOR DEVICE, SEMI-CONDUCTOR DEVICE AND RECORDING MEDIUM

## **REQUEST FOR RECONSIDERATION**

ASSISTANT COMMISSIONER FOR PATENTS WASHINGTON, D.C. 20231

SIR:

In Response to the Office Action mailed May 20, 2002, please note the following remarks traversing the rejection of Claims 1-5 and 18.

## **REMARKS**

Favorable reconsideration of this application is respectfully requested.

Claims 1-20 are pending in this application. Claims 17 and 20 have been withdrawn from consideration.

The outstanding Action presents a rejection of Claims 1-5 and 18 as being obvious over Iwamatsu et al (the 1995 article entitled "High-Speed 0.5 µm SOI 1/8 Frequency Divider with Body-Fixed Structure for Wide Range of Applications) (Iwamatsu) in view of Agari (JA 6-224302), Chen et al (U.S. Patent No. 5,767,549)(Chen), Blake et al (U.S. Patent No. 4,899,202, Blake), Gunning (U.S. Patent No.5,023,488), and Masuda et al (U.S. Patent No.